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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.: 10/708,715

Conf. No.: 2714

Filing Date: 3/19/2004

Art Unit:

2825

Applicant:

Bard et al.

Examiner:

Lin, Sun L.

Title:

INTEGRATED CIRCUIT DESIGN

Docket No.: BUR920030172US1

FOR SIGNAL INTEGRITY, AVOIDING

(IBMB-0039)

WELL PROXIMITY EFFECTS

COMMISSIONER FOR PATENTS

DESTINATION FACSIMILE NUMBER:

571-273-8300

Transmitted herewith is:

Amendment in 11 pages

in the above identified application.

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Docket No.: BUR920030|172US1

(IBMB-0039)

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INTEGRATED CIRCUIT DESIGN FOR SIGNAL INTEGRITY,

AVOIDING WELL PROXIMITY EFFECTS

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT

Sir:

I. INTRODUCTORY COMMENTS:

In response to the Office Action of December 20, 2005, please reconsider the abovereferenced patent application in view of the following amendments and remarks:

Amendments to the Claims are reflected in the listing of claims that begin on page 2 of this paper.

Remarks begin on page 7 of this paper.

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